K. RAMAKRISHNAN COLLEGE OF ENGINEERING
(Approved by AICTE & Affiliated to Anna University)Tiruchirappalli – 621 112
Accredited by NAAC with 'A' Grade- ISO 9001: 2015 Certified Institution

DIGITAL SYSTEMS LABORATORY

Laboratory In-charge: Mrs.V.Brinda AP/ECE

Technical supporting staff : Mrs.K.Kasthuri

Snapshot of Digital Systems Laboratory

Area of the laboratory: 92.39 Sq.m

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Major Equipment:

- Personal Computer - INTEL PENTIUM DUAL CORE
- Digital IC Trainer Kit
- Digital IC Tester
- HDL simulator

List of Experiments:

- Verification of Boolean Theorems using basic gates
- Design and implementation of combinational circuits using basic gates for arbitrary functions, code converters
- Design and implement Half/Full Adder and Subtractor
- Design and implement of various combinational circuits using MSI devices
- Design and implement shift registers, synchronous and asynchronous counters
- Coding combinational and sequential circuits using HDL
- Design and implementation of a simple digital system

Beyond the syllabus experiments:

- Simple IC 4011 LED Flasher Circuit
- Simulation of Sequential Circuits

Utilization of the laboratory:

- Digital Systems Laboratory for II year CSE /III sem